

**IN THE CLAIMS**

This listing of claims will replace all prior versions, and listings, of claims in the application.

**Listing of Claims:**

1. (Previously presented) A method for measuring and trimming the impedance of a driver device in a semiconductor device during a test being carried out before the regular operation of the semiconductor device, the driver device of the semiconductor device including each a pull-up circuit and a pull-down circuit, the method comprising:

joint activating both the pull-up circuit and the pull-down circuit; and

determining a first current flowing through the pull-up circuit or the pull-down circuit, respectively, with jointly activated pull-up and pull down circuits during the test carried out before the regular operation of the semiconductor device.

2. (Currently amended) A method for measuring and trimming the impedance of a driver device in a semiconductor device during a test being carried out before the regular operation of the semiconductor device, the driver device of the semiconductor device including each a pull-up circuit and a pull-down circuit, the method comprising:

joint activating both the pull-up circuit and the pull-down circuit; and

determining a first current flowing through the pull-up circuit or the pull-down circuit, respectively, with jointly activated pull-up and pull down circuits during the test carried out before the regular operation of the semiconductor device;

~~The method according to claim 1,~~ wherein the pull-up or pull-down circuits, respectively, are connected to a supply voltage pad or a ground connection, respectively, of the semiconductor device, and the method further comprising:

joint de-activating both the pull-up circuit and the pull-down circuit; and

determining a standby current flowing between the supply voltage pad and the ground connection with jointly de-activated pull-up and pull down circuits.

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